

Atty Docket No. 4860.P2438

Patent

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:

Sara Biyabani

Serial No. 09/589,621

Filed: June 7, 2000

For: DECOUPLING A COLOR  
BUFFER FROM MAIN  
MEMORY

) Examiner: A. Caschera

) Art Unit: 2628

) Confirmation No. 8620

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

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02/26/2007 SSESHE1 00000031 09589621

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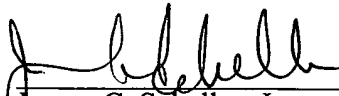
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Respectfully submitted,

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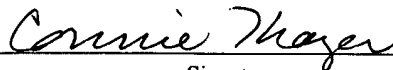
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## Sheet

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of

2

Application Number	09/589,621
Filing Date	06/07/2000
First Named Inventor:	Sara Ruhina Biyabani
Art Unit	2628
Examiner Name	Caschera, Antonio A.
Attorney Docket No.	04860.P2438

[illegible][illegible]Date  
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				Application Number	09/589,621
				Filing Date	06/07/2000
				First Named Inventor:	Sara Ruhina Biyabani
				Art Unit	2628
				Examiner Name	Caschera, Antonio A.
Sheet	2	of	2	Attorney Docket No.	04860.P2438
<b>NON PATENT LITERATURE DOCUMENTS</b>					
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T <sup>2</sup>
		JUPITERMEDIA CORPORATION. "Integrated Video: Breaking the last PC Taboo," 802.11 Planet Conference & Expo World Tour, downloaded from the Internet at: <a href="http://www.cpubplanet.com/features/article.php/1566521">www.cpubplanet.com/features/article.php/1566521</a> , © 2003 Jupitermedia Corporation (2 pages).			
		KOCSIS, DAVID. "Frame-buffer wars: new directions in PC graphics," <u>EDN Access for Design, by Design</u> , May 9, 1996 (10 pages).			
		SILICON GRAPHICS, INC. "O <sup>2</sup> Unified Memory Architecture," <u>Silicon Graphics, Inc.</u> , 1997 (pp. 1-6).			
		WILLIAMS, IAN. "An Explanation of the Advantages Afforded by QED RM5200 and MIPS R12000 CPUs in O2," SGI White Paper, 1999 (pp. 1-41).			

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